

1 ABSTRACT OF THE DISCLOSURE

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3 Apparatus for simultaneously transferring synchronous and
4 asynchronous signals among broadband access devices includes a
5 data bus, a clock bus, and a plurality of control lines which are
6 used to indicate the type of data being carried on the bus.
7 According to the methods of the invention, data is transferred on
8 the bus in a repeating frame having a plurality of slots, each
9 slot being defined as one bus clock cycle. Each slot may contain
10 a synchronous or asynchronous data signal and one or more of the
11 control lines are asserted during the slot time of the data to
12 indicate the type of data. Two embodiments are provided. One
13 utilizes a 25 MHz clock bus and a repeating frame of three hundred
14 thirty-six slots. The other utilizes a 75 MHz clock bus and a
15 repeating frame of one thousand eight slots.